

INFORMATION DISCLOSURE CITATION
IN AN APPLICATION

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FEB 26 2002

Document Number (Continuation)

ERD-01-001

Filing Date

10/01/01

Applicant

David Meltzer

Filing Date

12/14/01

Office Address

2631

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILED DATE & APPROPRIATE
QG	6215834	4/10/01	McCollough	375	375	8/4/97
QG	5963608	10/5/99	Casper et al.	375	373	6/26/97
QG	5886582	3/23/99	Stansell	331	1A	8/7/96
QG	5870002	2/9/99	Ghadery et al.	331	17	6/23/97
QG	5838749	11/17/98	Casper et al.	375	376	6/5/95
QG	5822387	10/13/98	Mar	375	376	3/25/96
QG	5724007	3/3/98	Mar	331	1A	3/25/96
QG	5394444	2/28/95	Silvey et al.	375	374	7/12/93
QG	4499434	2/12/85	Thompson	331	17	7/19/82
QG	5359727	10/25/94	Kurita et al.	395	550	3/2/90
QG	5463329	10/31/95	Kawasaki et al.	326	77	4/8/94

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Portion of Pages, Etc.)

QG	"Phase-Lock Loop Based (PLL) Clock Driver: A Critical Look at Benefits Versus Costs," Nayan Patel, SCAA033A, Texas Instruments, March 1997.
QG	"Phase-Locked Loops for High-Frequency Receivers and Transmitters - Part 1," Curtin and O'Brian, Analog Dialogue, 33-3, 1999.

EXAMINER

DATE COMPLETED

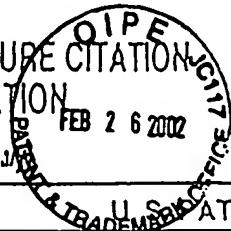
9/20/05

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10/016,915

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	INDEX DATE & APPROPRIATE
QG	5382923	1/17/95	Shimada et al.	331	8	4/5/94
QG	5570042	10/29/96	Ma	326	63	1/3/95
QG	5581214	12/3/96	Iga	331	16	8/30/95
QG	5793225	8/11/98	Gerson	326	68	1/2/96
QG	5905386	5/18/99	Gerson	326	73	2/27/98
QG	6246294	6/12/01	Gai	331	57	2/12/99
QG	6285262	9/4/01	Kuriyama	331	25	6/29/99
QG	5614843	3/25/97	Mita et al.	326	73	1/5/96
QG	5889437	3/30/99	Lee	331	16	7/11/97
QG	6111469	8/29/00	Adachi	331	17	8/20/98
QG	4736167	4/5/88	Kojima et al.	331	17	6/23/86

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Part/Serial Pages, Etc.)

QG	The TQ8103 Clock + Data Recovery (CDR) product description manufactured by Triquint Semiconductor Inc. of a monolithic CDR IC.
QG	The AMCC 53040A Device Specification (April 2, 2000) of a clock recovery unit.

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PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	TITLE	CLASS	SUBCLASS	FILED DATE IF APPROPRIATE
QG	4818950	4/4/89	Ranger	328	155	4/24/87
QG	5151665	9/29/92	Wentzler	331	8	2/7/91
QG	5157354	10/20/92	Saiki et al.	331	1A	11/27/91
QG	5216302	6/1/93	Tanizawa	307	603	9/18/91
Patent App. 00 QG 2001	13800A	8/16/01	Wu et al.	327	156	7/22/99

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Part/Invent Pages, Etc.)

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